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04/05/2005

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<i>Sandra L. Carr</i>	(Depositor's name)
<i>Sandra L. Carr</i>	(Signature)
7-5-05	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/617,980	07/10/2003	Edward A. Ramsden	M-15145 US	4887

TITLE OF INVENTION: PROGRAMMABLE LOGIC DEVICE WITH HARDWIRED MICROSEQUENCER 07/05/2005 MBIZUNE2 00000026 502257 10617980

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APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	07/05/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHANG, DANIEL D	2819	326-046000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Jonathan Hallman
 MacPherson Kwok Chen
 2. & Heid
 3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.111. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Lattice Semiconductor Corporation

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

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Date

7/1/05

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42,1022

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ramsden et al.
Assignee: Lattice Semiconductor Corporation
Title: PROGRAMMABLE LOGIC DEVICE WITH HARDWIRED MICROSEQUENCER
Serial No.: 10/617,980 Filing Date: July 10, 2003
Examiner: Chang, D. Group Art Unit: 2819
Docket No.: M-15145 US Confirmation No. 4887

Irvine, California
June 30, 2005

Fax No.: 1-703-746-4000

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ISSUE FEE TRANSMITTAL**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following Part B – Fee(s) Transmittal is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: 7-5-05

By: Saundra L. Carr
Saundra L. Carr

Total Pages Being Transmitted: 2

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